

# "Exhibit A"

Page 364 of MC68000 microprocessor.

Signetics Military Customer Specific Products

Product Specification

## 16-/32-Bit Microprocessor

68000

### ORDERING INFORMATION

DESCRIPTION	ORDER CODE	SPEED
64-Pin Ceramic DIP 000mil-wide	68000-6/BXA	6MHz
	68000-8/BXA	8MHz
	68000-10/BXA	10MHz
68-Pin Ceramic LCC	68000-6/BUG	6MHz
	68000-8/BUG	8MHz
	68000-10/BUG	10MHz

In supervisor mode, the upper byte of the status register and the supervisor stack pointer (SSP) are also available to the programmer. These registers are shown in Figure 2.

The status register, Figure 3, contains the interrupt mask (eight levels available) as well as the condition codes: extend (X), negative (N), zero (Z), overflow (V), and carry (C). Additional status bits indicate that the processor is in trace (T) mode and in a supervisor (S) or user state.

